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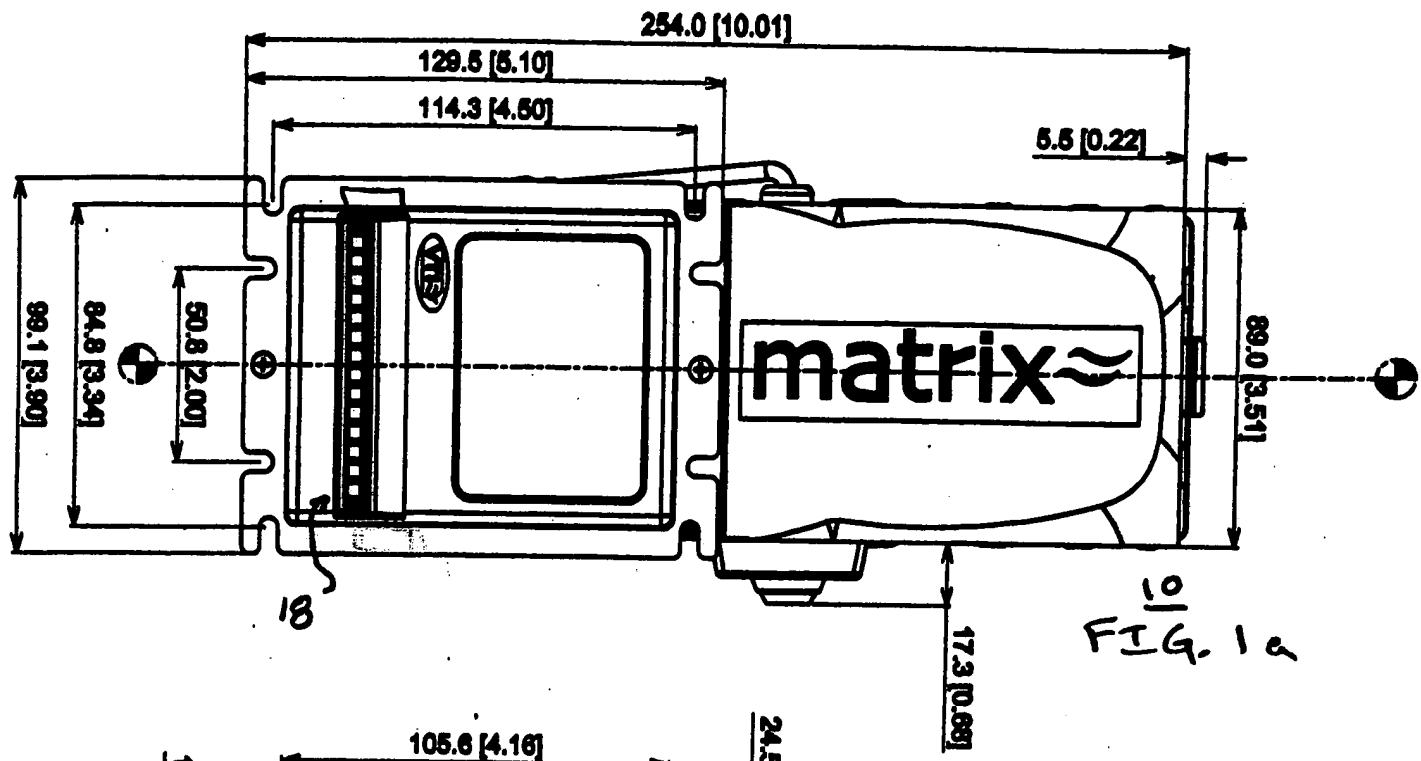


FIG. 1a

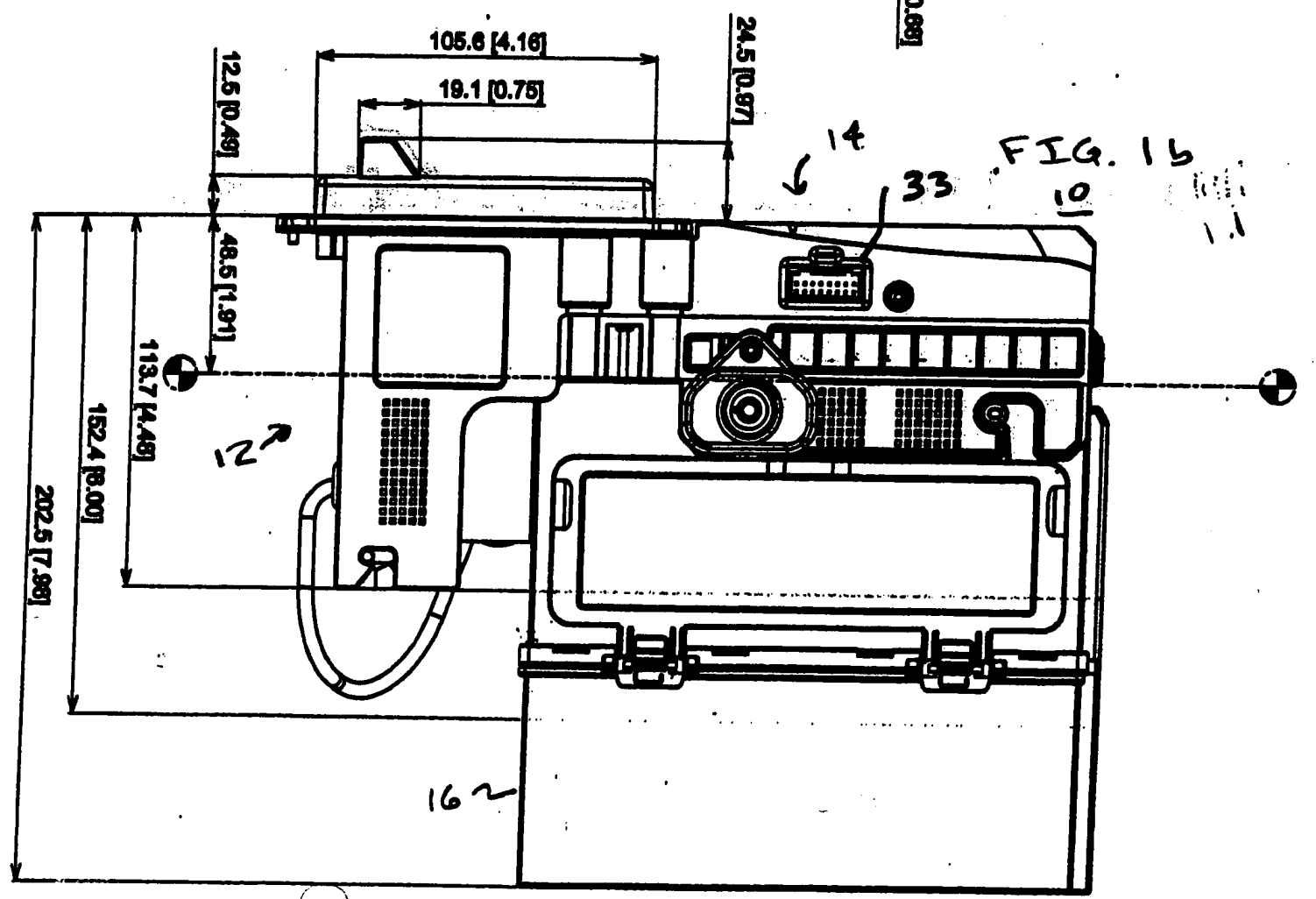


FIG. 1b

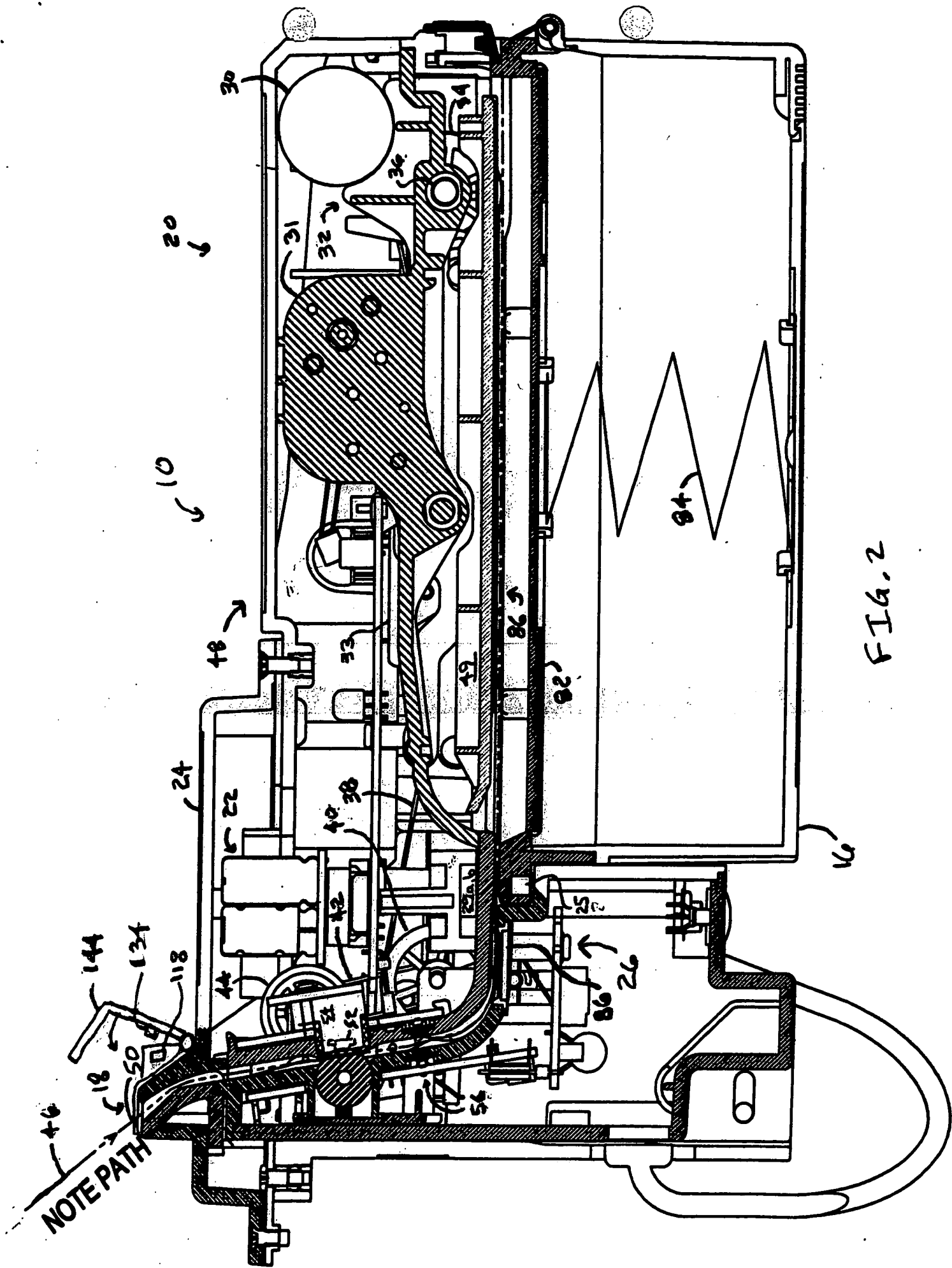


FIG. 2

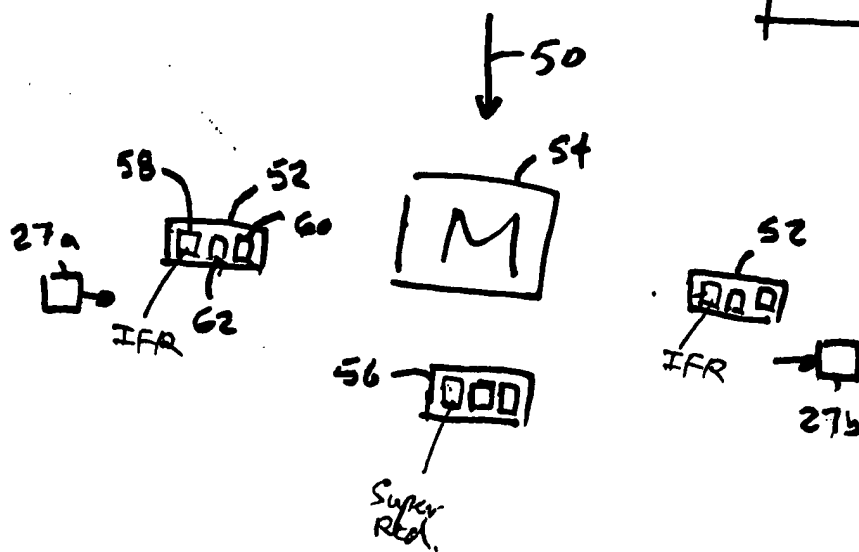
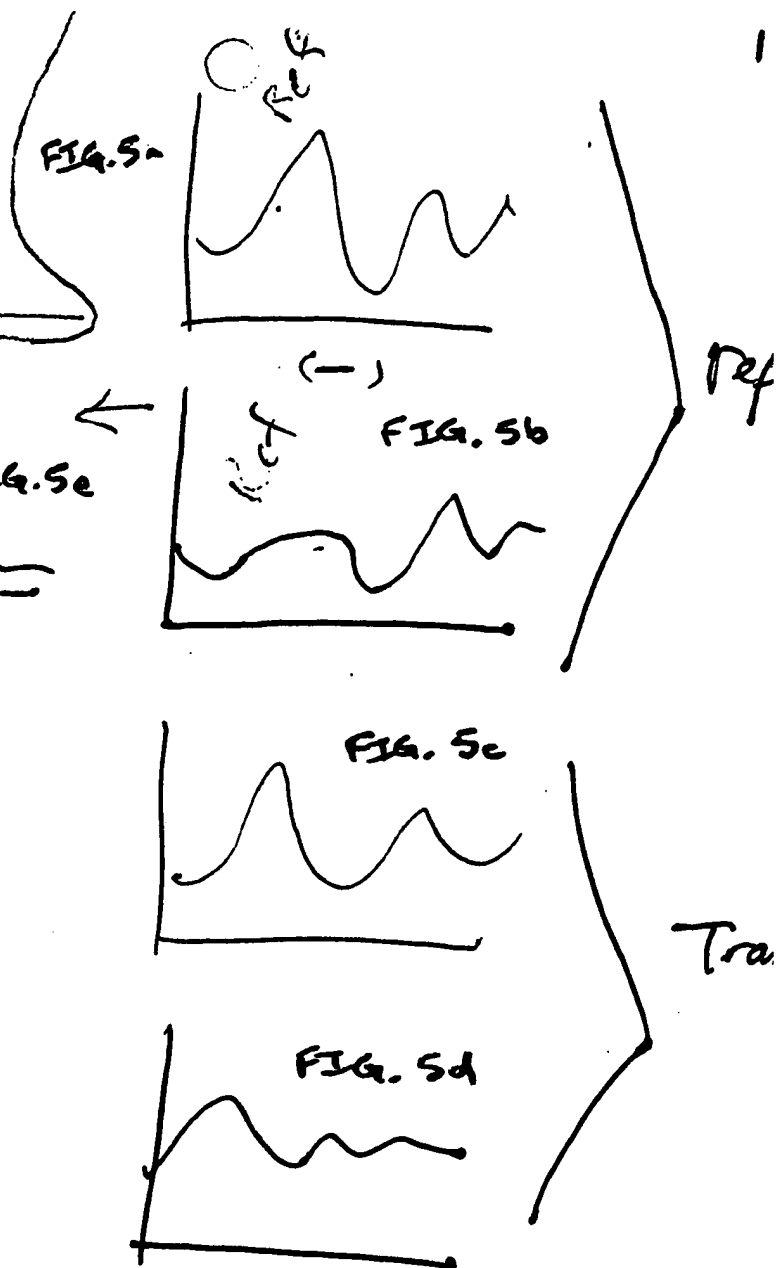
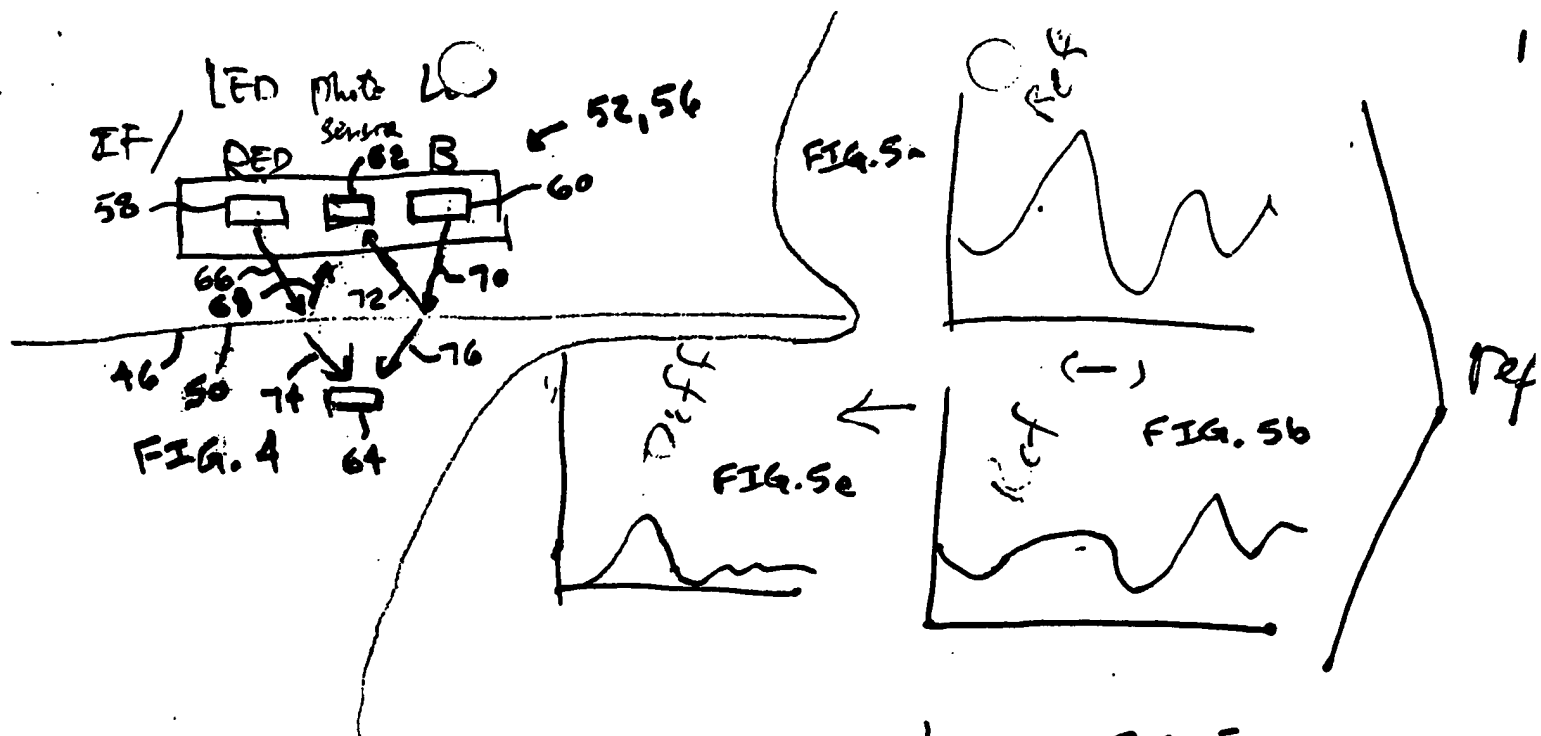


FIG. 3

The diagram illustrates a VMEbus system architecture, divided into two main sections: the Upper Head Assembly and the Lower Head Assembly.

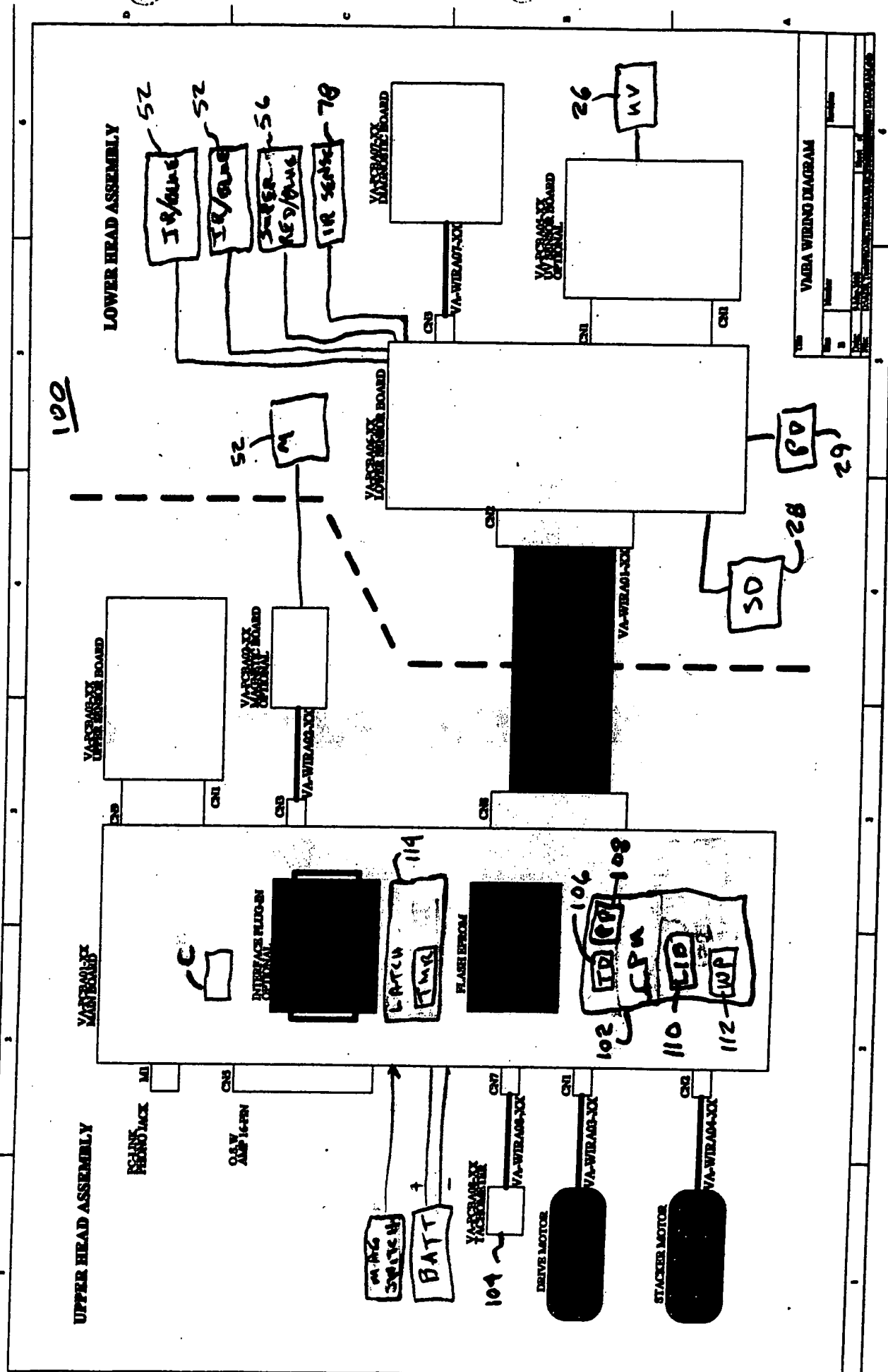
Upper Head Assembly:

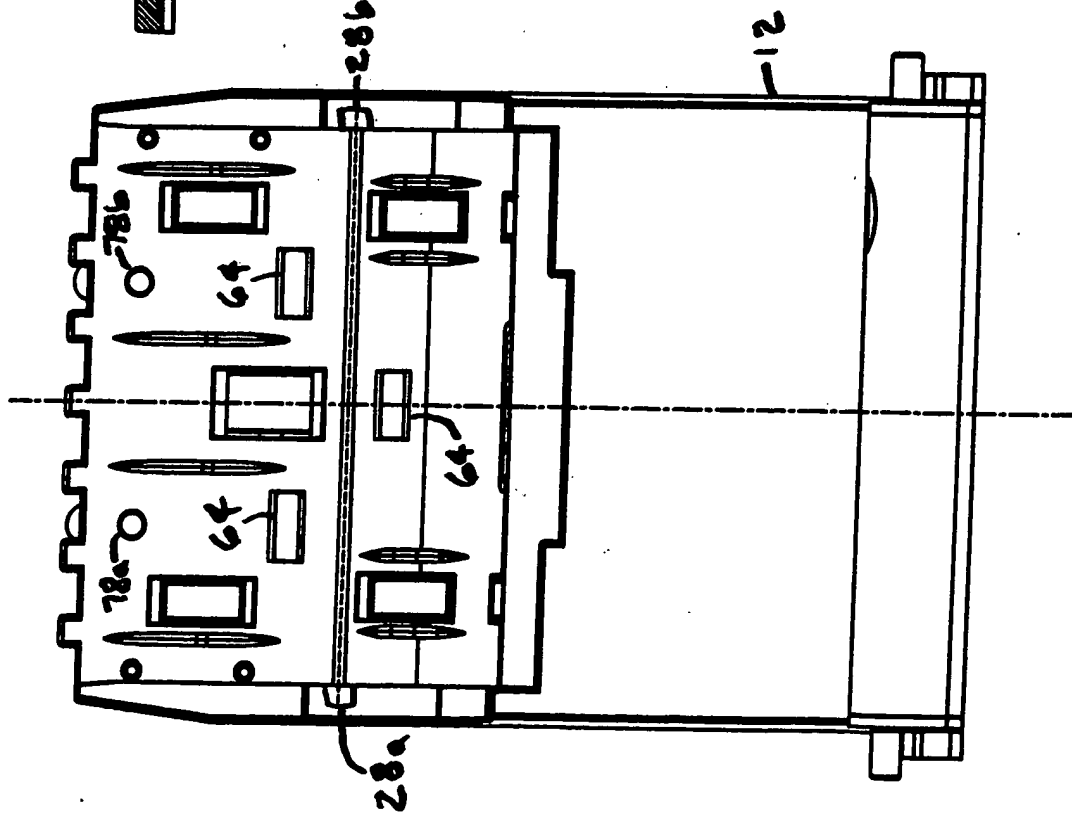
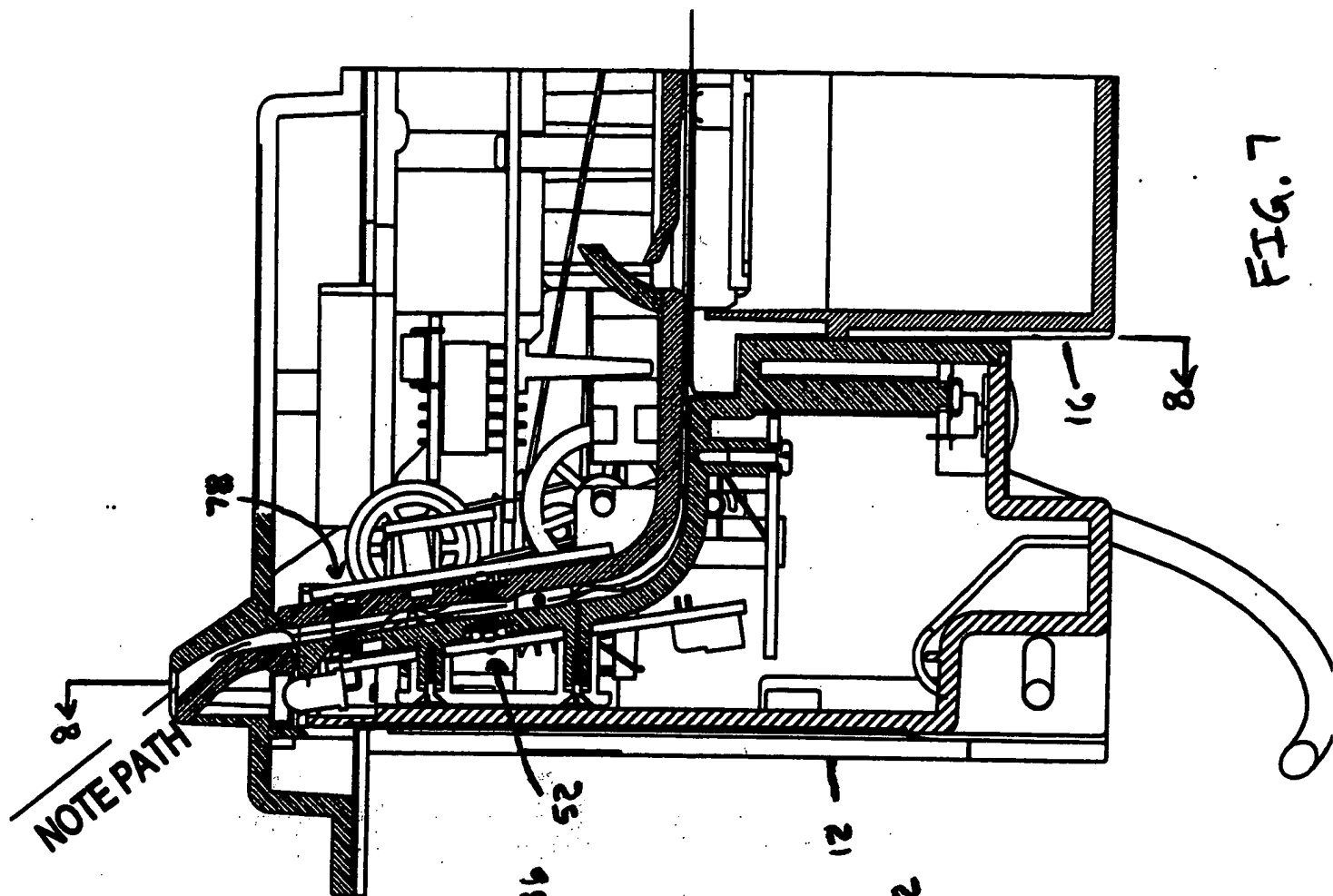
- Central Processing Unit (CPU):** Labeled 102, it is connected to the VMEbus (104).
- Local Resource Unit (LRU):** Labeled 110, it is connected to the VMEbus (104).
- Work Processing Unit (WPU):** Labeled 112, it is connected to the VMEbus (104).
- Battery (BATT):** Labeled 104, it is connected to the VMEbus (104).
- Drive Motor:** Connected to the VMEbus (104).
- Stacker Motor:** Connected to the VMEbus (104).

Lower Head Assembly:

- VMEbus (106):** A central bus connecting the upper and lower assemblies.
- VMEbus (108):** A bus connecting the lower assembly to the VMEbus (106).
- VMEbus (110):** A bus connecting the lower assembly to the VMEbus (106).
- VMEbus (112):** A bus connecting the lower assembly to the VMEbus (106).
- VMEbus (114):** A bus connecting the lower assembly to the VMEbus (106).
- VMEbus (116):** A bus connecting the lower assembly to the VMEbus (106).

The diagram is labeled "VMEbus" and "VMEbus".





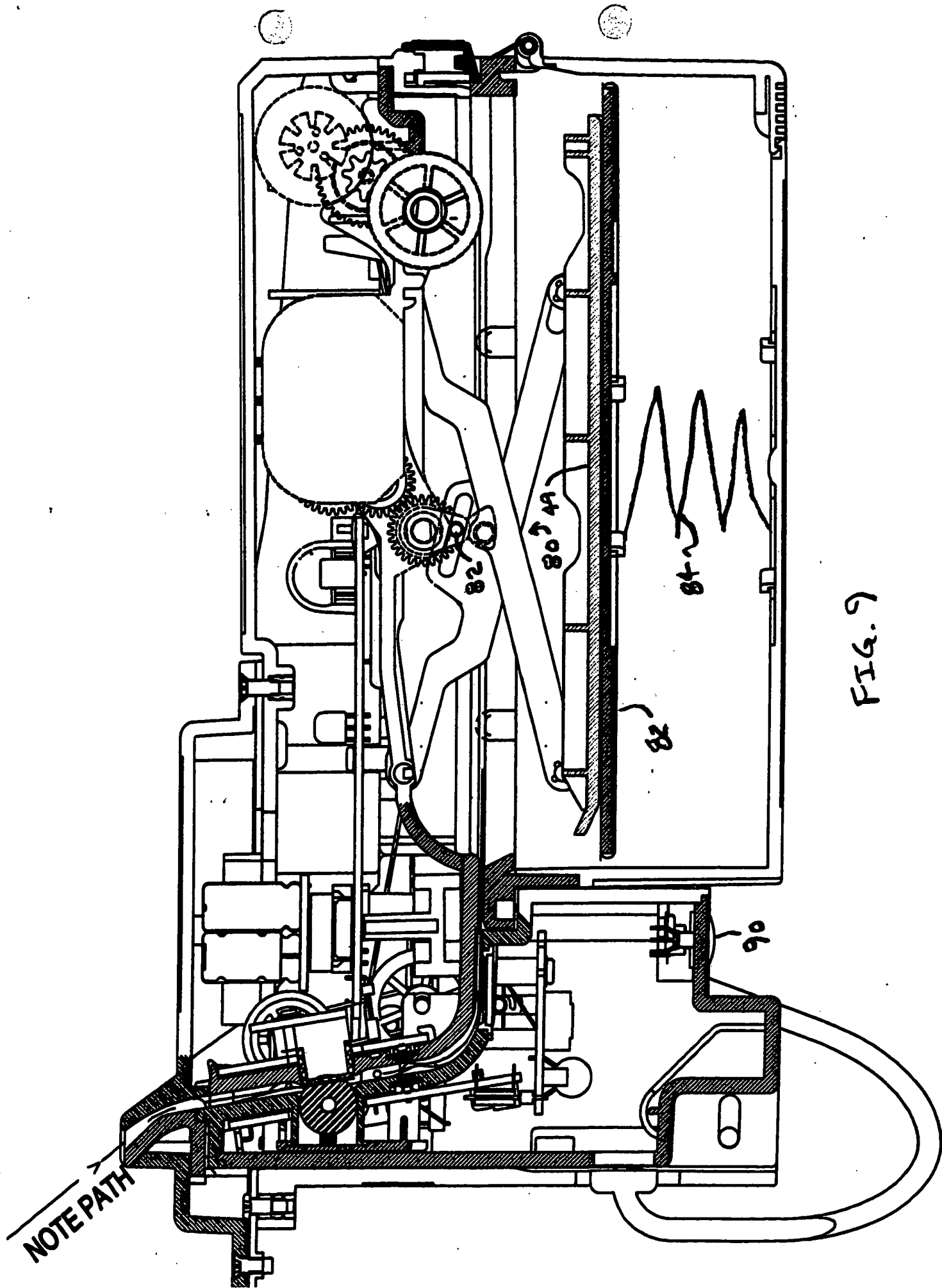
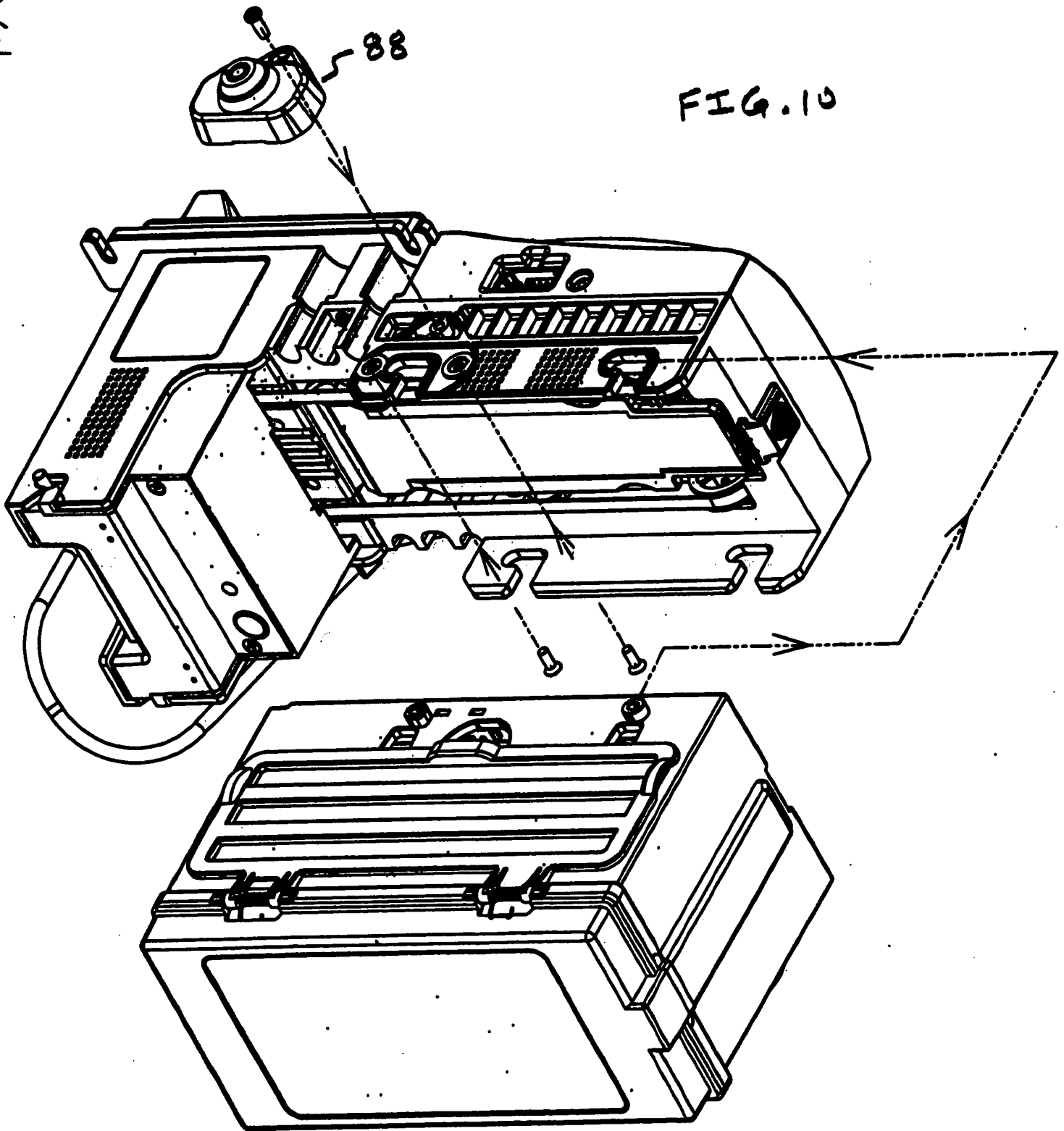


FIG. 9

SCALE: 0.6=1



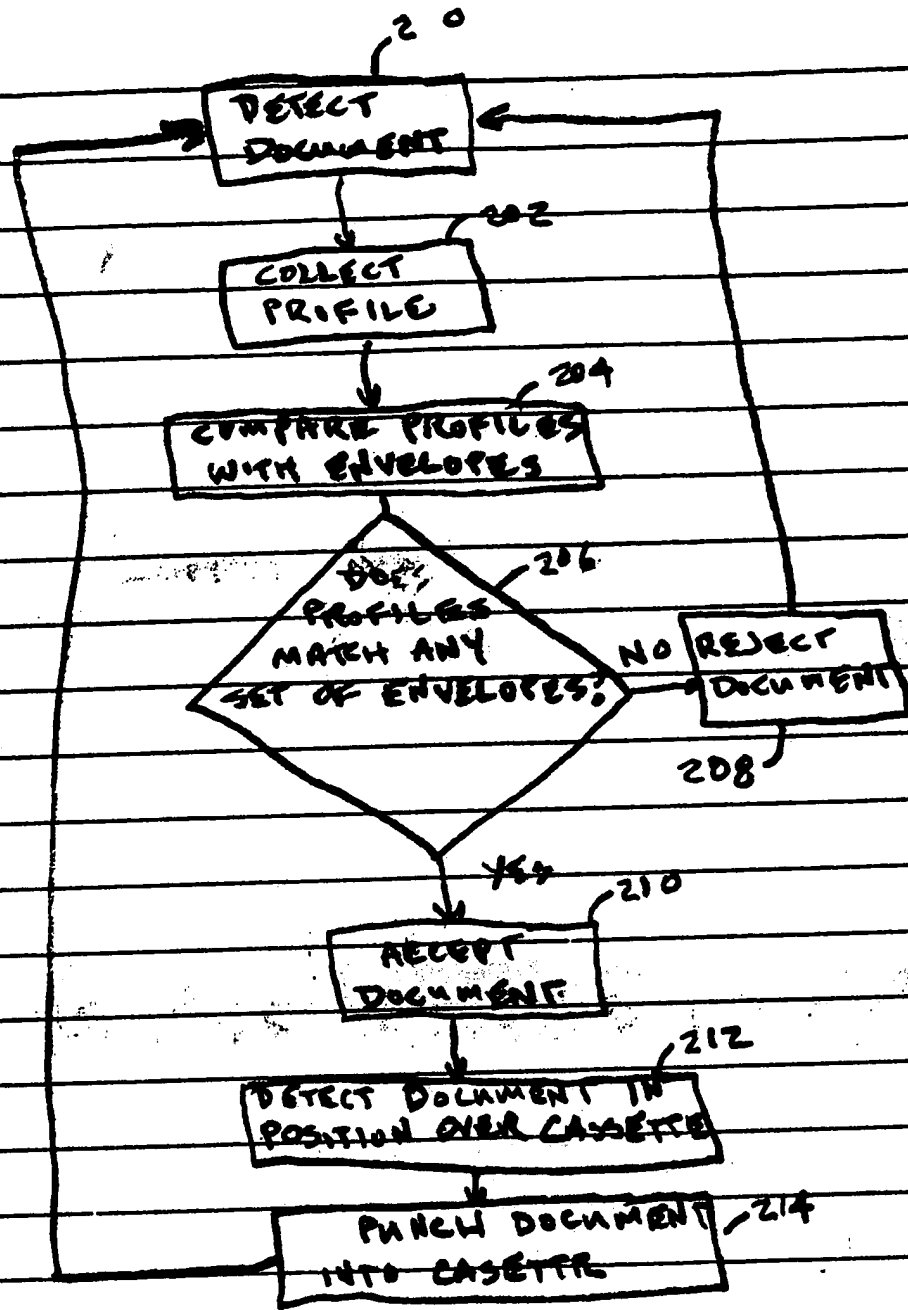


FIG. 11

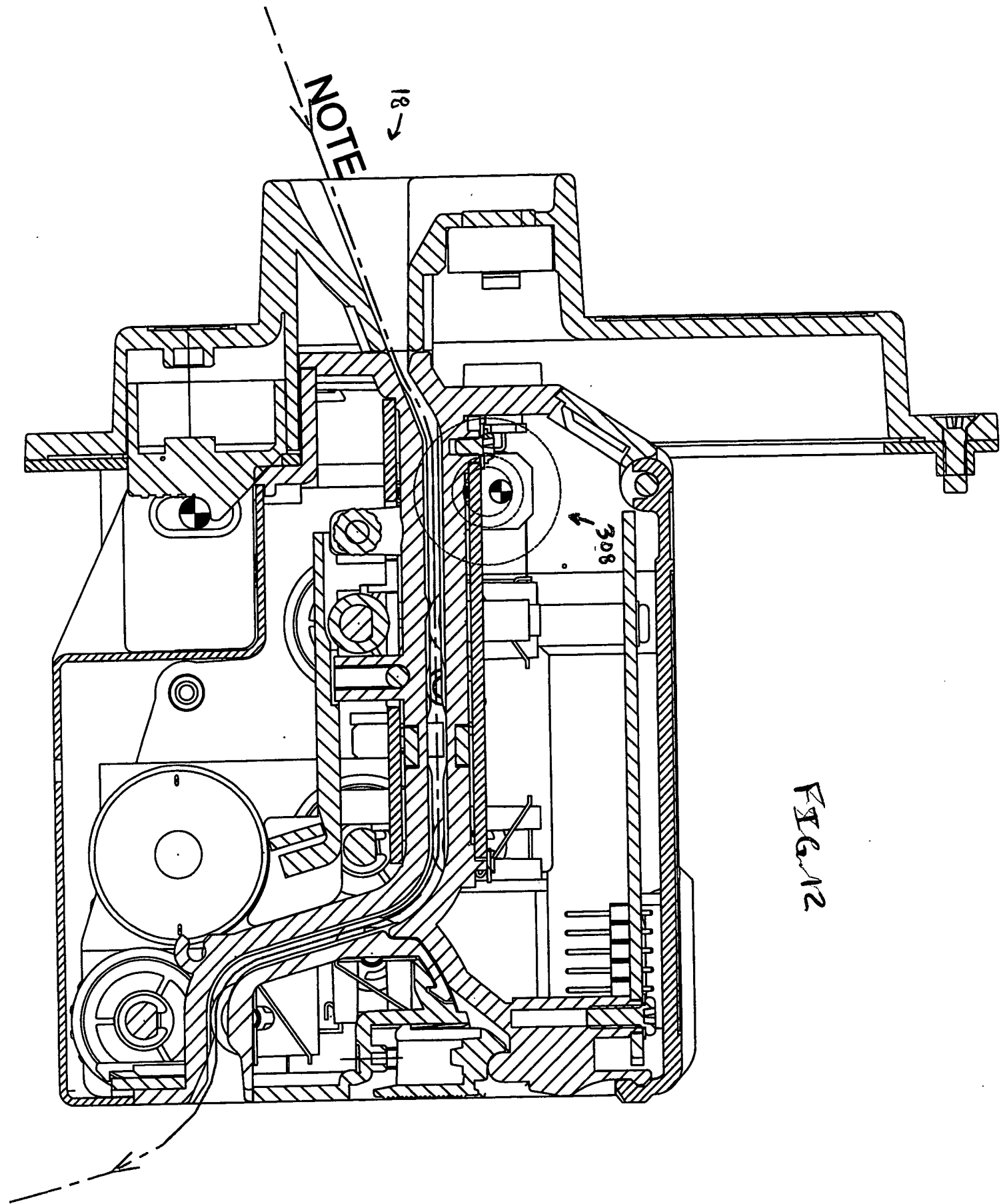


FIG. 12

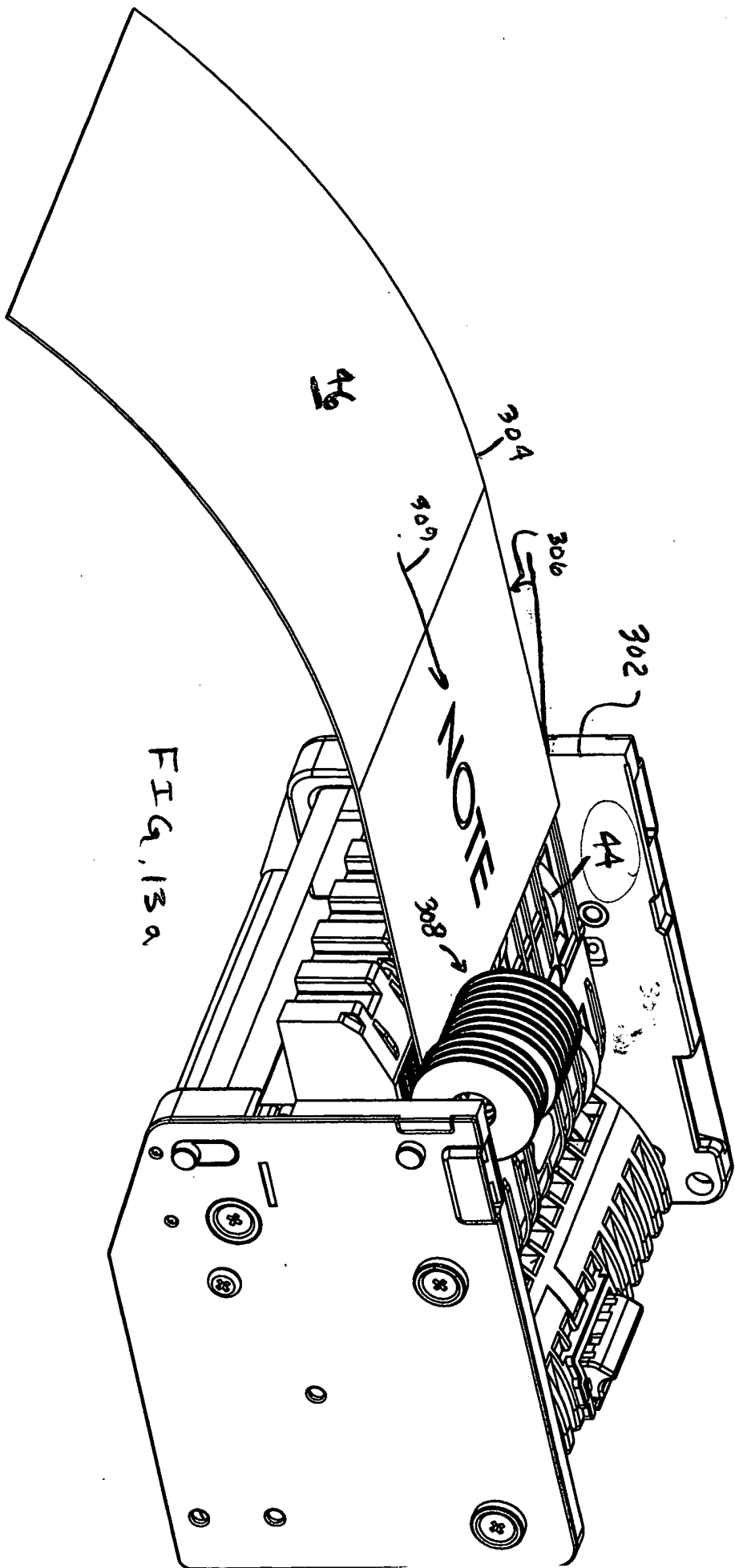


FIG. 13a

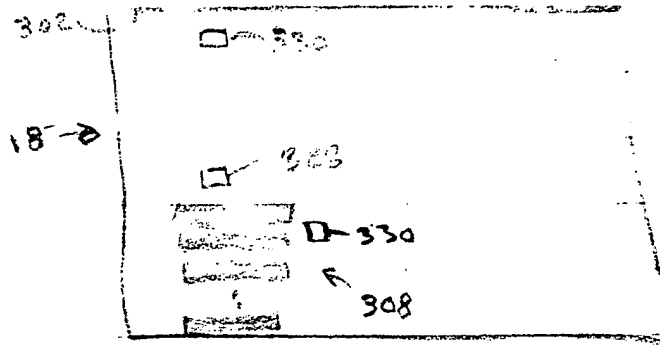
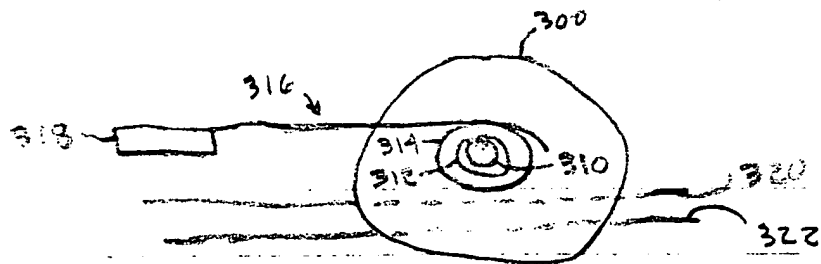
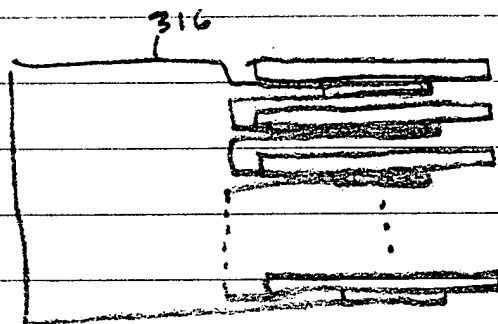


FIG. 136



308
FIG. 14a



308
FIG. 14b

